Patch review check list

- If it has inline asm
  - Does it use specific instructions, does it correctly handle cores w/o such instructions
  - Does it correctly specify argument constraints (e.g. earlyclobber (=&) arguments)
  - Does it use possibly reserved registers (e.g. a7 with CONFIG_FRAME_POINTER)

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http://wiki.osll.ru/doku.php/etc:users:jcmvbkbc:linux-xtensa:patch-review-checklist?rev=1397084660

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