Linux port for Xtensa

Phase 1

Goal: linux mainline contains the latest linux-xtensa updates, builds and works.

- compilable with latest GCC
- overlays to cover: FSF, dc232b, more TBD
- tests to pass: builds, boots, LTP
- boards to focus on: ISS, ML605
- configurations: UP, linux-next specific, more TBD

Plan

- Prepare for linux-next participation
  - [3/3] Make mainline build for ARCH=xtensa in defconfig, allnoconfig, allmodconfig
    - [+] fix remaining issues with allmodconfig
      - concept of IO space? IO mapping?
    - [+] try other fix for literals.unlikely
    - why there's no simdisk in the mainline?
  - [2/3] Make built images work (qemu, FSF)
- Build userspace
- Analyse reference tree

From:
http://wiki.osll.ru/ - **Open Source & Linux Lab**

Permanent link:
http://wiki.osll.ru/doku.php/etc:users:jcmvbkbc:linux-xtensa?rev=1342711319

Last update: **2012/07/19 19:21**