Linux port for Xtensa

Phase 1

Goal: linux mainline contains the latest linux-xtensa updates, builds and works.

- compilable with latest GCC
- overlays to cover: FSF, dc232b, more TBD
- tests to pass: builds, boots, LTP
- boards to focus on: ISS, ML605
- configurations: UP, linux-next specific, more TBD

Plan

- Prepare for linux-next participation
  - [2/3] Make mainline build for ARCH=xtensa in defconfig, allnoconfig, allmodconfig
    - [-] fix remaining issues with allmodconfig
      - concept of IO space? IO mapping?
    - why there's no simdisk in the mainline?
    - qemu needs proper handling for guest ioctls triggered via simcalls
  - [+] Make built images work (qemu, FSF)
- [+] Build userspace
- Automated build/test scripts
  - [+] build/test status/logs
  - expect script to run LTP
- Analyse reference tree
  - proper bugfixes
  - new cores (dc233c, test_hifi_c3, s*)
  - new platforms (lx*, ml605, s*)
  - SMP and its fixes
  - XCC fixes

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