

QEMU support for Xtensa

Things to do:

- core/basic opcodes implementation;
 - [+] and/or/xor/neg/abs;
 - [+] shifts;
 - [+] add[x*]/sub[x*]/add.n/addi.n;
 - [+] call0, callx0, j, b*;
 - [+] l32*, s32*;
 - [-] windowed registers, call*/callx*;
 - [-] boolean registers/commands;
- MMU;
 - [+] no-mmu mode;
- gdbserver;
 - [+] xml register map, read/write register;
 - [+] debug exception, single step mode;
 - [-] hw/sw breakpoints;
 - [+] wrong disassembly (caused by wrong endianness of produced ELF);
- exceptions;
 - [+] debug;
- sample evaluation board;
 - [+] memory mapping, ELF loader;
 - [-] standard peripherals.

From:

<http://wiki.osll.ru/> - **Open Source & Linux Lab**

Permanent link:

<http://wiki.osll.ru/doku.php/etc:users:jcmvbkbc:qemu-target-xtensa?rev=1300830144>

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