

# QEMU support for Xtensa

## Things to do:

- core/basic opcodes implementation;
  - [+] and/or/xor/neg/abs;
  - [+] shifts;
  - [+] add[x\*]/sub[x\*]/add.n/addi.n;
  - [+] call0, callx0, j, b\*;
  - [+] l32\*, s32\*;
  - [-] windowed registers, call\*/callx\*;
  - [-] boolean registers/commands;
- windowed registers;
  - [+] simple overflow algorithm that's triggered from ENTER;
- loop option;
- floating point;
- MMU;
  - [+] no-mmu mode;
- gdbserver;
  - [+] xml register map, read/write register;
  - [+] debug exception, single step mode;
  - [-] hw/sw breakpoints;
  - [+] wrong disassembly (caused by wrong endianness of produced ELF's);
- exceptions;
  - [+] debug;
- sample evaluation board;
  - [+] memory mapping, ELF loader;
  - [-] standard peripherals.

From:

<http://wiki.osll.ru/> - **Open Source & Linux Lab**

Permanent link:

<http://wiki.osll.ru/doku.php/etc:users:jcmvbkbc:qemu-target-xtensa?rev=1301389055>

Last update: **2011/03/29 12:57**

