

QEMU support for Xtensa

Git tree: <http://jcmvbkbc.spb.ru/git/?p=dumb/qemu-xtensa.git;a=shortlog;h=refs/heads/xtensa>

Things to do:

- core/basic opcodes implementation;
 - [+] and/or/xor/neg/abs;
 - [+] shifts;
 - [+] add[x*]/sub[x*]/add.n/addi.n;
 - [+] call0, callx0, j, b*;
 - [+] l32*, s32*;
 - [+] windowed registers, call*/callx*, retw, rotw, rfwo, rfwu;
 - [-] boolean registers/commands;
- windowed registers;
 - [+] simple overflow algorithm that's triggered from ENTER;
- loop option;
- floating point;
- MMU;
 - [+] no-mmu mode;
- gdbserver;
 - [+] xml register map, read/write register;
 - [+] debug exception, single step mode;
 - [-] hw/sw breakpoints;
 - [+] wrong disassembly (caused by wrong endianness of produced ELF's);
- exceptions;
 - [+] debug;
 - [+] window overflow/underflow;
 - [-] invalid instruction;
- sample evaluation board;
 - [+] memory mapping, ELF loader;
 - [-] standard peripherals;
- external configuration (a-la xtensa overlay)?

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<http://wiki.osll.ru/doku.php/etc:users:jcmvbkbc:qemu-target-xtensa?rev=1302857929>

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